

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: KIMURA
FILED: March 5, 2002
FOR: CMOS REFERENCE VOLTAGE CIRCUIT
DOCKET: NEC NEG-244

BOX PATENT APPLICATION
Assistant Commissioner for Patents & Trademarks
Washington, D.C. 20231

#3
4/29/02
J1040 U.S. PTO
10/091776
03/05/02

PRIOR ART DISCLOSURE STATEMENT

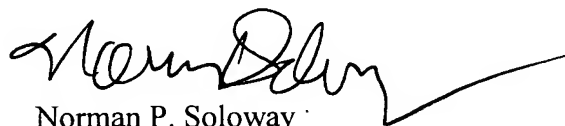
Dear Sir:

In connection with the above-entitled matter, Applicant hereby attaches U.S. Patent Office Form PTO-1449, including copies of the references listed therein. The reference is discussed in the specification. The claims in the present application are believed to be patentably distinguished over these references.

This information disclosure statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the information thus disclosed in fact constitutes prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,



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